

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	1115594
<b>Application Number:</b>	09751748
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<b>Title of Invention:</b>	CONFIGURABLE CO-PROCESSOR INTERFACE
<b>First Named Inventor:</b>	Lawrence Henry Hudepohl
<b>Customer Number:</b>	23669
<b>Filer:</b>	James Weldon Huffman/Taysie Locke
<b>Filer Authorized By:</b>	James Weldon Huffman
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### Payment information:

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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Information Disclosure Statement (IDS) Filed	MIPS_0107_00_US_IDS.pdf	132475	no	2

<b>Warnings:</b>					
<b>Information:</b>					
This is not an USPTO supplied IDS fillable form					
2	NPL Documents	EGGERS_Techniques_for_Efficient_Inline_Tracing_.pdf	1034516	no	11
<b>Warnings:</b>					
<b>Information:</b>					
3	NPL Documents	UHLIG_Trace-Driven_Memory_Simulation_A_Survey.pdf	3971964	no	43
<b>Warnings:</b>					
<b>Information:</b>					
4	NPL Documents	ROTENBERG_Trace_Processors.pdf	1206885	no	12
<b>Warnings:</b>					
<b>Information:</b>					
5	NPL Documents	ELNOZAHY_Address_Trace_Compression_Through_Loop_Detection.pdf	238481	no	2
<b>Warnings:</b>					
<b>Information:</b>					
6	NPL Documents	JONES_MIPS64_5kc_Processor_Cores_Users_Manual_Rev1_July_4_1999.pdf	1555191	no	28
<b>Warnings:</b>					
<b>Information:</b>					
7	NPL Documents	CHOW_Engineering_A_RISC_Compiler_System_.pdf	597702	no	8
<b>Warnings:</b>					
<b>Information:</b>					
8	NPL Documents	SMITH_Tracing_with_Pixie_.pdf	1472472	no	29
<b>Warnings:</b>					
<b>Information:</b>					
9	NPL Documents	ATOM_Reference_Manual_December_1993.pdf	1413359	no	32
<b>Warnings:</b>					
<b>Information:</b>					

10	NPL Documents	SRIVASTAVA_ATOM_A_Sy stem_for_Building_Customiz ed_.pdf	1482837	no	27
<b>Warnings:</b>					
<b>Information:</b>					
11	NPL Documents	ATOM_User_Manual_March _1994.pdf	1328015	no	29
<b>Warnings:</b>					
<b>Information:</b>					
12	NPL Documents	CMELIK_Shade_A_Fast_Ins truction-Set_Simulator.pdf	1275263	no	10
<b>Warnings:</b>					
<b>Information:</b>					
13	NPL Documents	MD00014_MIPS64_5Kc_Pro cessor_Core_Datasheet_Re v_1_7_5_.pdf	2670025	no	40
<b>Warnings:</b>					
<b>Information:</b>					
14	NPL Documents	MIPS64_5Kc_Processor_Co re_Datasheet_Rev_2_0_MD 00014.pdf	2733558	no	44
<b>Warnings:</b>					
<b>Information:</b>					
15	NPL Documents	MD00109_MIPS64_5Kf_Pro cessor_Core_Datasheet_Re v_00_11.pdf	3428325	no	44
<b>Warnings:</b>					
<b>Information:</b>					
16	NPL Documents	MD00106_MIPS64_5K_Proc essor_Core_Family_Integrat ors_Guide_Rev02_00_.pdf	4428383	no	83
<b>Warnings:</b>					
<b>Information:</b>					
17	NPL Documents	MD00056_MIPS64_5Kc_Pro cessor_Core_Integrators_Gu ide_Rev1_2_.pdf	3777313	no	86
<b>Warnings:</b>					
<b>Information:</b>					
18	NPL Documents	MD00068_MIPS_Core_Copr ocessor_Interface_Specificat ion_Rev1_11.pdf	1576512	no	26
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**New Applications Under 35 U.S.C. 111**

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

**National Stage of an International Application under 35 U.S.C. 371**

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.